

What is claimed is:

1. A driving method of a plasma display panel in which an  $n^{\text{th}}$  frame having a first brightness weighting value and a  $(n+1)^{\text{th}}$  frame having a brightness weighting value different from the first brightness weighting value are alternatively arranged every vertical synchronous signal to display a certain image,

wherein one frame period of the  $n^{\text{th}}$  frame or the  $(n+1)^{\text{th}}$  frame is variably set such that a brightness expression period can be identically set at the  $n^{\text{th}}$  frame and the  $(n+1)^{\text{th}}$  frame.

2. The driving method according to claim 1, wherein each of the  $n^{\text{th}}$  frame and the  $(n+1)^{\text{th}}$  frame comprises:

a reset period during which a uniform wall charge is formed in a discharge cell;

an address period during which an address discharge is generated to select the discharge cell; and

a sustain period during which a sustain discharge is generated at predetermined times depending on a gray level value, in the discharge cell where the address discharge is generated.

3. The driving method according to claim 1, wherein the frame period is varied by the address period or the sustain period.

4. The driving method according to claim 3, wherein the address period varying the frame period is varied by increase or decrease of a first period during which the wall charge formed during the address period is maintained.

5. The driving method according to claim 3, wherein the sustain period varying the frame period is varied by increase or decrease of a second period during which the wall charge formed during the sustain period is maintained.

6. The driving method according to claim 3, wherein the address period and the sustain period are differently varied depending on an average picture level (APL).

7. The driving method according to claim 6, wherein if the APL is at a low level, the address period and the sustain period are shortened.

8. The driving method according to claim 6, wherein if the APL is at a high level, the address period and the sustain period are lengthened.

9. The driving method according to claim 1, wherein the frame period is varied by both of the address period and the sustain period.

10. A driving method of a plasma display panel in which a certain image is respectively displayed by a selective write and selective erase frame driven in a 60Hz mode and a selective write and selective erase frame driven in a 50Hz mode,

wherein one frame period of the selective write and selective erase frame driven in the 60Hz mode or the selective write and selective erase frame driven in the 50Hz mode is variably set such that a brightness expression period can be identically set at the selective write and selective erase frame driven in the 60Hz mode and the selective write and selective erase frame driven in the 50Hz mode.

11. The driving method according to claim 10, wherein each of the selective write and selective erase frame driven in the 60Hz mode and the selective write and selective erase frame driven in the 50Hz mode, comprises:

at least one selective write sub-field having a reset period during which a uniform wall charge is formed at a discharge cell, an address period during which an address discharge is generated to select the discharge cell, and a sustain period during which a sustain discharge is generated in the discharge cell where the address discharge is generated at predetermined times depending on a gray level value; and

at least one selective erase sub-field having an address period during which an address discharge is generated to select the discharge cell, and a sustain period during which a sustain

discharge is generated in the discharge cells where the address discharge is generated at predetermined times depending on a gray level value.

12. The driving method according to claim 10, wherein the frame period is varied by at least one period among the address period of the selective write sub-field, the sustain period of the selective write sub-field, the address period of the selective erase sub-field, and the sustain period of the selective erase sub-field.

13. The driving method according to claim 12, wherein the address period of the selective write sub-field varying the frame period is varied by increase or decrease of a first period during which the wall charge formed during the address period of the selective write sub-field is maintained.

14. The driving method according to claim 12, wherein the sustain period of the selective write sub-field varying the frame period is varied by increase or decrease of a second period during which the wall charge formed during the sustain period of the selective write sub-field is maintained.

15. The driving method according to claim 12, wherein the address period of the selective erase sub-field varying the frame period is varied by increase or decrease of a third period during

which the wall charge formed during the address period of the selective erase sub-field is maintained.

16. The driving method according to claim 12, wherein the sustain period of the selective erase sub-field varying the frame period is varied by increase or decrease of a fourth period during which the wall charge formed during the sustain period of the selective erase sub-field is maintained.

17. The driving method according to claim 12, wherein the address period of the selective write sub-field, the sustain period of the selective write sub-field, the address period of the selective erase sub-field and the sustain period of the selective erase sub-field are differently varied depending on an AVL.

18. The driving method according to claim 17, wherein if the AVL is at a low level, the address period of the selective write sub-field, the sustain period of the selective write sub-field, the address period of the selective erase sub-field and the sustain period of the selective erase sub-field are shortened.

19. The driving method according to claim 10, wherein if the AVL is at a high level, the address period of the selective write sub-field, the sustain period of the selective write sub-

field, the address period of the selective erase sub-field and the sustain period of the selective erase sub-field are shortened.